



CATRENE / MEDEA+ Design Technology Conference in association with the edaWorkshop

Nano-Electronics Towards System Integration: The EDA Challenge

Highlights from CATRENE/MEDEA+ projects

26 – 28 May 2009

Dorint Hotel, Dresden, Germany

Focus

The Design Technology Conference, as successor of the MEDEA+ Design Automation Conference, will focus on application-oriented design methods for SoC in themselves a very important success factor for European Micro- Electronic companies. Exhaustive research and development in this area has been supported by CATRENE/MEDEA+ and local governments through the past five years. Latest results and exciting highlights from CATRENE/MEDEA+ projects will form the subjects of this conference.

Organisation

Sponsored by CATRENE

Local Organisation: Ralf Popp, edacentrum, Germany

Conference Chair: W. Rosenstiel

Technical Programme Chair: A. Jerraya

Contents: Call for Contributions

All CATRENE/MEDEA+ projects related to electronic system design, to design automation and to manufacturing topics related to design will contribute to the conference. In addition to key notes and panels the following sessions are planned:

- System Level Design: F. Petrot
- 3D Integration Design & Technology: M. Diaz-Nava
- Synthesis & Verification for Analog circuits: R. Popp
- Design for Yield: G. Georgakos

Conference location

Dorint Hotel, Dresden, Germany

Steering Committee

- * J. Borel, J.B - R&D, F
- * B. Candaele, Thales Group, F
- * M. Coppola, STMicroelectronics, F
- * A-M. Fouilliant, Thales Group, F
- * A. Jerraya, CEA-LETI, F
- * W. John, SIL, D
- * P. Koch, M+/CATRENE EU
- * TBC, DS2, E
- * C. Sebeke, Bosch, D
- * W. Rosenstiel, Uni. Tuebingen & FZI, D
- * TBC, Infineon, D
- * M. Diaz-Nava, STMicroelectronics, F
- * K. Veelenturf, NXP Semiconductors, NL
- * F. Petrot, TIMA, F
- * E. Villar, Uni. Cantabria, E
- * M. Dietrich, FhG, D

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