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1. Eureka/MEDEA+ projects drive European nanoelectronics to production

Several EUREKA projects are contributing to the development of future nanometer CMOS generations. As a result of MEDEA+ work, 90 nm node technologies are already in industrial production. The 65-nm node is reaching the product prototyping stage and first choices for a 45-nm technology are available with work continuing towards full process integration well in line with the International Technology Roadmap for Semiconductors (ITRS). Industrial exploitation of 90-nm CMOS industrial technology is possible based on the rules for industrial fabrication developed in the MEDEA+ T201 CMOS logic 0.1 μm project. In the year following the end of T201, 25 submicron circuits were processed at Crolles 2, at the joint Freescale, Philips Semiconductors and STMicroelectronics pilot 300-mm wafer facility at Grenoble in France. MEDEA+ T207 65nm CMOS300 involved new substrate materials as well as multilevel interconnect metallization for 65nm circuit nodes. The 65-nm process has been established with significant yield improvements and reliability specifications and is now ready for the manufacture of prototype customer chips. The chipmaking partners will be sharing their 65-nm cell libraries and IP blocks are confident about the success of the process to be produced from 2008. And EUREKA projects are already being planned to exploit the results of the EU Sixth Framework Programme (FP6) PULLNANO project that is looking further ahead at the needs for 32-/22-nm scale circuitry.

EUREKA is a pan-European network for market-oriented, industrial R&D. MEDEA+, the industry-initiated pan-European Programme for advanced co-operative R&D in Microelectronics, has been set up and labelled within the framework of EUREKA.

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