2005 RELEASE OF MEDEA+ EDA ROADMAP

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With the 5th release of the MEDEA+ Roadmap for Electronic Design Automation (EDA), key areas of design and research are highlighted with the goal of keeping the European Semiconductor Industry on the leading edge and providing an intriguing glimpse into the future of Design Automation.

Approaching the era of nano structures it appears that the times of "happy scaling" are reaching their end. New design flow paradigms and strong links between System Level Design and Design for Manufacturability (DfM) are becoming mandatory to master new technologies and to slow down the increasing design gap which has persisted for many years.

This latest roadmap release joins prime knowledge of a network of European Design experts from industry, research institutes and universities and confirms that DfM and further progress in SoC and SiP are all amongst vital areas of research.

The new roadmap addresses these challenges and extends its scope to the MEMS domain that represents a major pillar of the "embedded systems driven" European Semiconductor Industry and the need for "Smart Systems Integration Technology".

The roadmap is available for downloading from the MEDEA+ public website, but also available on CD-Rom on written request.