First Call for Participation

CATRENE
Design Technology Conference
in association with the edaWorkshop

Nano-Electronics Towards System Integration: The EDA Challenge

Highlights from CATRENE projects
10 – 12 May 2011
Dorint Hotel, Dresden, Germany

Focus

The Design Technology Conference will focus on application-oriented design methods for SoC including heterogeneous functions and SiP integration in themselves a very important success factor for European Micro- and Nano- Electronic companies. Exhaustive research and development in this area has been supported by CATRENE and local governments through the past five years. Latest results and exciting highlights from mainly CATRENE/MEDEA+ projects will form the subjects of this conference.

Contents: Call for Contributions

All CATRENE projects related to electronic system design, to design automation and to manufacturing topics related to design will contribute to the conference. Each project may organize a full session made of one tutorial presentation (45 min.) and 2 technical presentations (20 min. each) or contribute to a global session with a tutorial or a technical presentation. Project leaders are invited to submit request for contribution to <ralf.popp@edacentrum.de> **before November 12th, 2010.**

Organisation

Sponsored by CATRENE
Technical Programme Chair: A. Jerraya, F.

Conference Chair: W. Rosenstiel, D.
Local Organisation: R. Popp, edacentrum, D.

Steering Committee

- ❖ J. Borel, J.B - R&D, F
- ❖ J. Calero, DS2, E
- ❖ B. Candaele, Thales Group, F
- ❖ M. Coppola, STMicroelectronics, F
- ❖ M. Coenen, EMC MCC, NL
- ❖ M. Diaz-Nava, STMicroelectronics, F
- ❖ M. Dietrich, FhG, D
- ❖ A-M. Fouilliart, Thales Group, F
- ❖ A. Jerraya, CEA-LETI, F
- ❖ W. John, SIL, D

- ❖ P. Koch, CATRENE EU
- ❖ P. Merkus Philips, NL
- ❖ F. Petrot TIMA, F
- ❖ R. Pferdmenges, Infineon, D
- ❖ C. Sebeke, Bosch, D
- ❖ W. Rosenstiel, U. Tuebingen & FZI, D
- ❖ D. Rousset , STE, F
- ❖ K. Veelenturf, NXP, NL
- ❖ E. Villar, U. Cantabria, E

Conference location

Dorint Hotel, Dresden, Germany

System Innovation on Silicon